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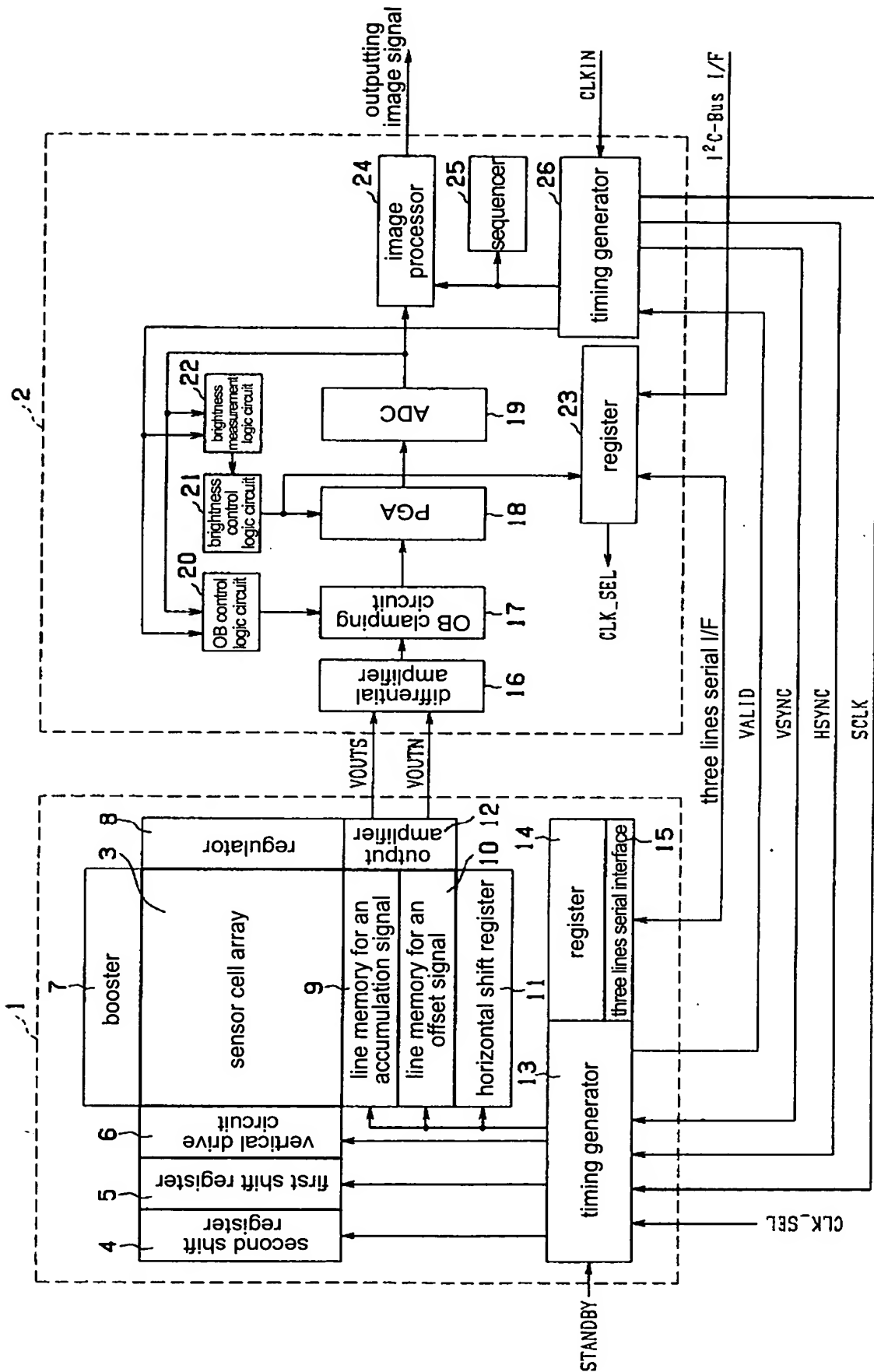


FIG. 1

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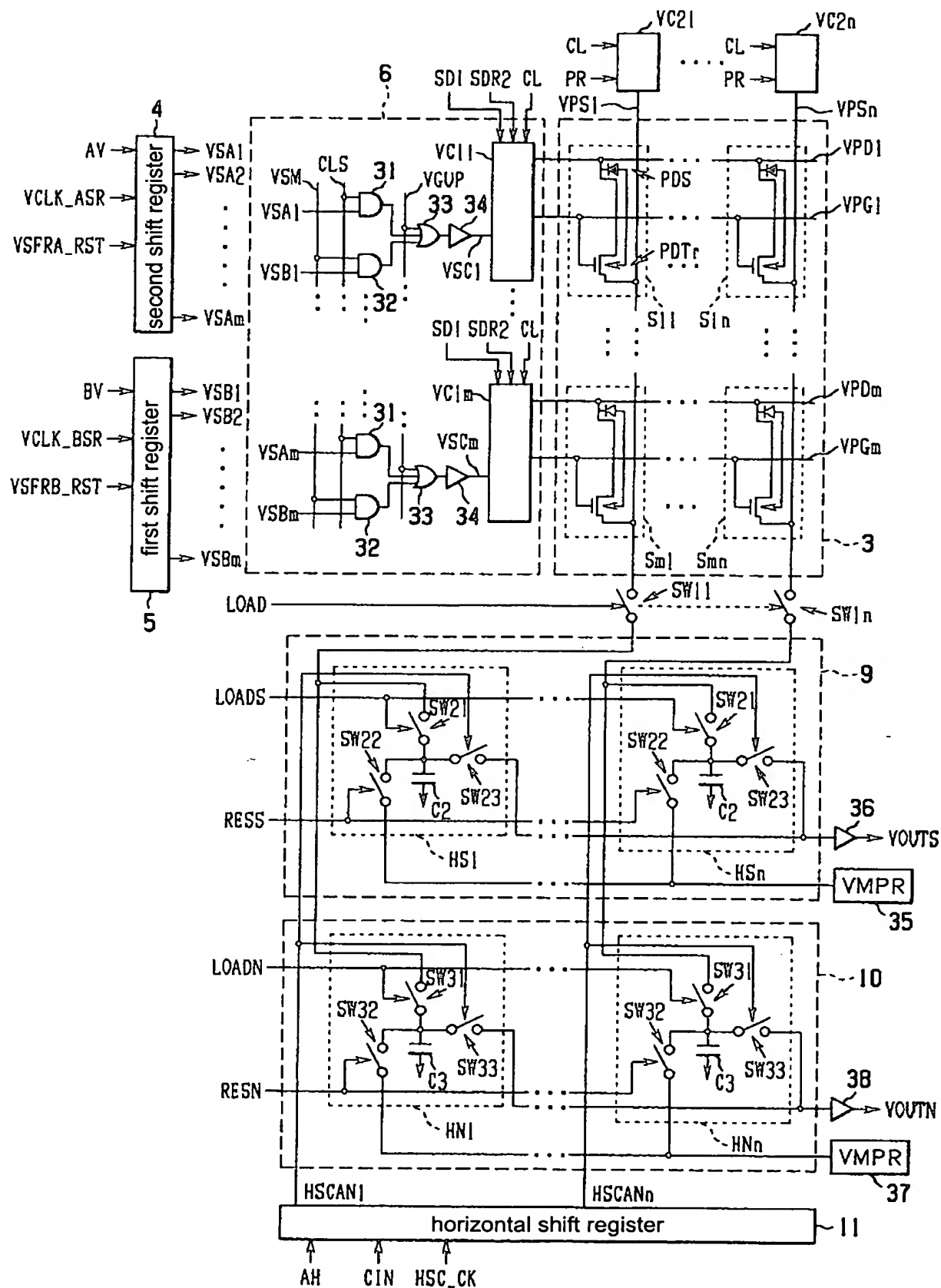


FIG. 2

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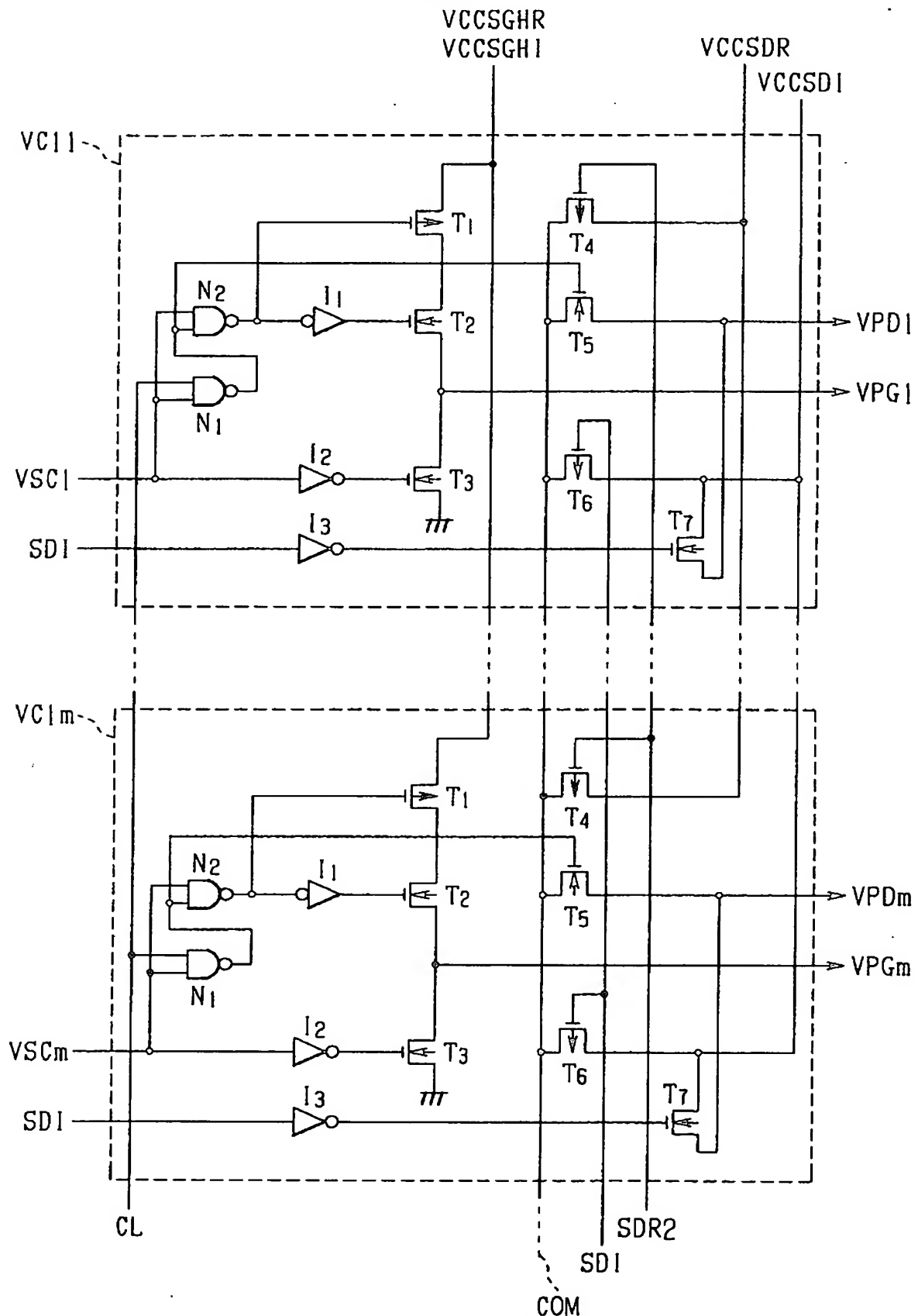


FIG. 3

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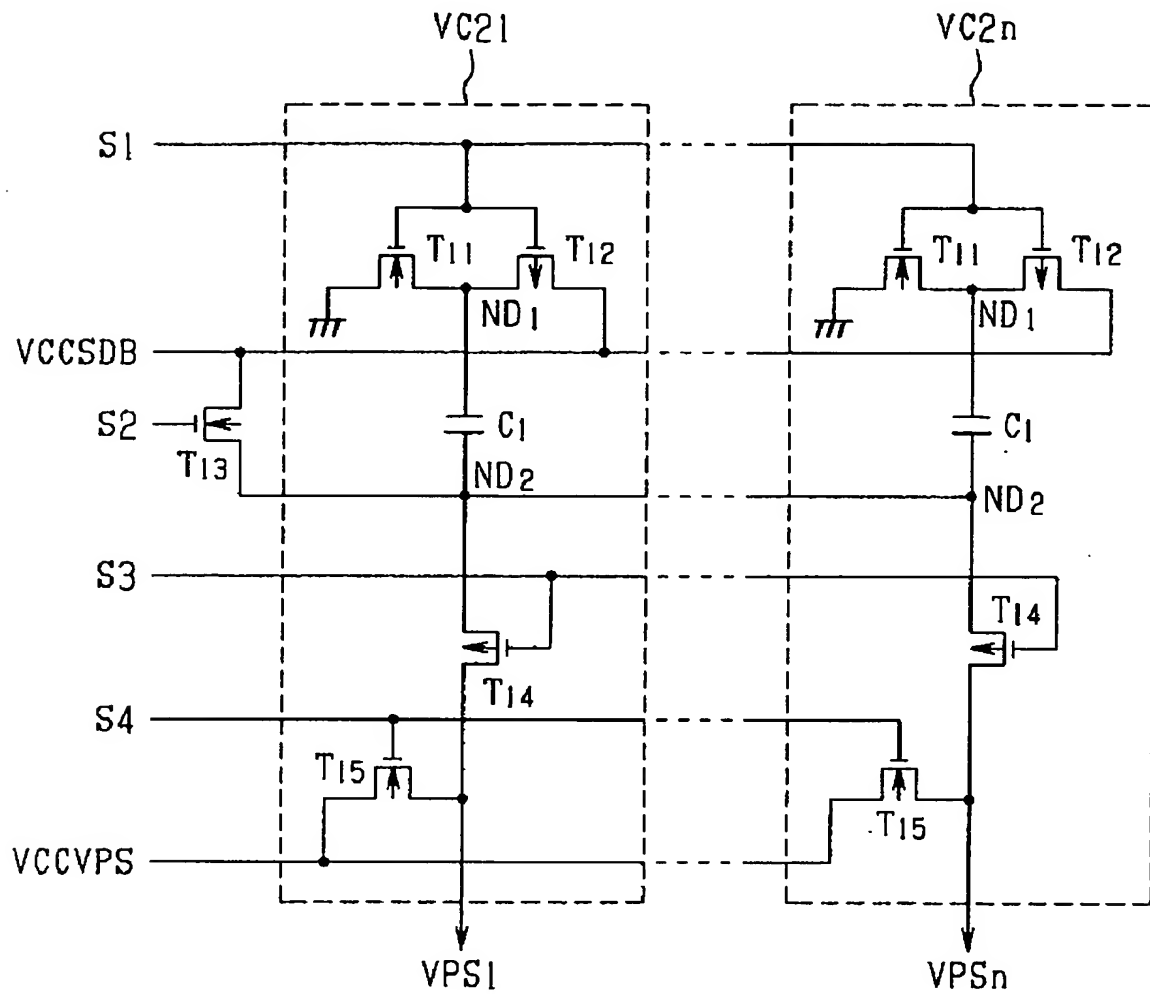


FIG. 4A

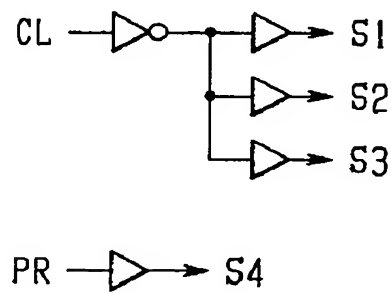


FIG. 4B

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States of sensor bias

	state	accumulation	RESS	LOADS	PR	CL	RESN	LOADN
GATE	selected	VCCSGHI	Lo	VCCSGHR	VCCSGHR	VCCSGHR+ VCCSDBX 2	LoI	VCCSGHR
	non selected		Lo	Lo	Lo	Lo	Lo	Lo
SOURCE	selected	turning "on" (VCCSDI)	VMPR	VCCSGHR-VthS	VCCVPS	VCCSDBX 2	VMPR	VCCSGHR-VthN
DRAIN	selected	VCCSDI	HiZ	VCCSDR	turning "on" (VCCVPS)	turning "on" (VCCSDBX 2)	HiZ	VCCSDR
	non selected		HiZ	VCCSDR	VCCVPS	VCCSDR	HiZ	VCCSDR

FIG. 5

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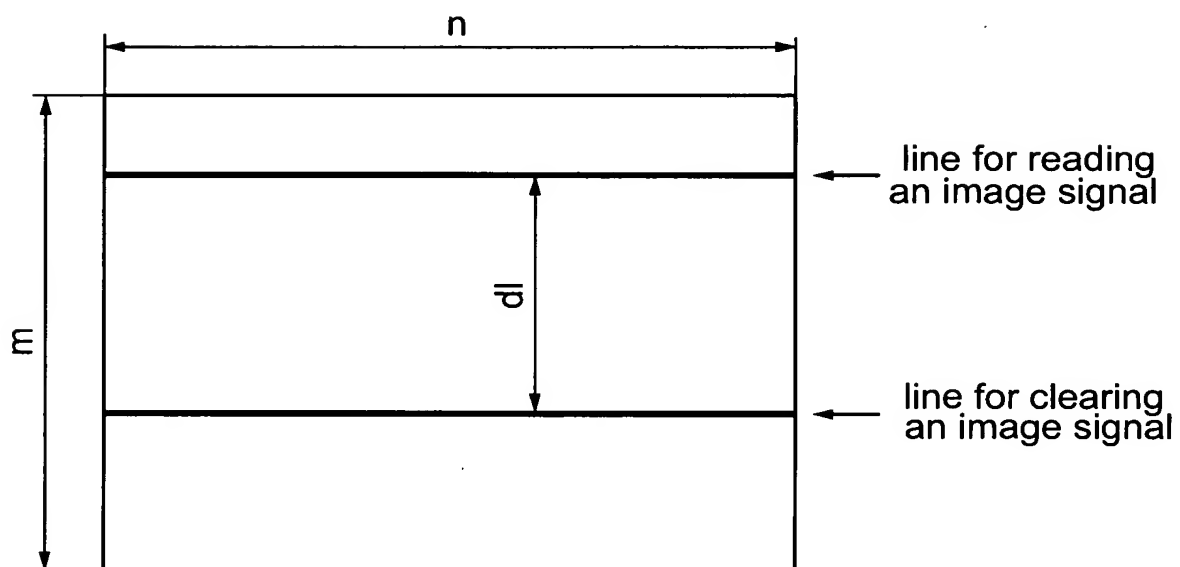


FIG. 6

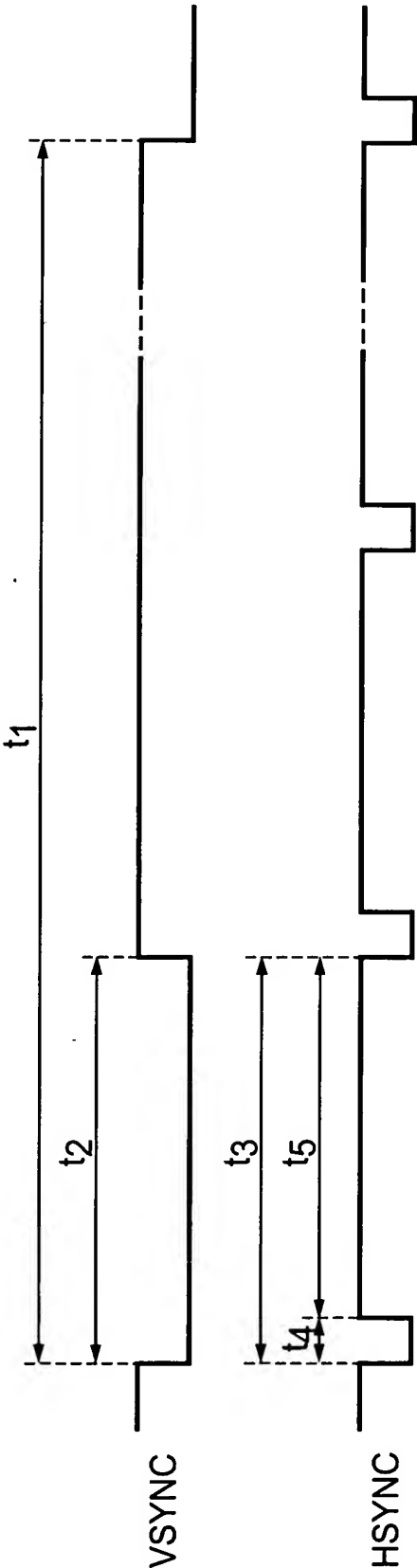
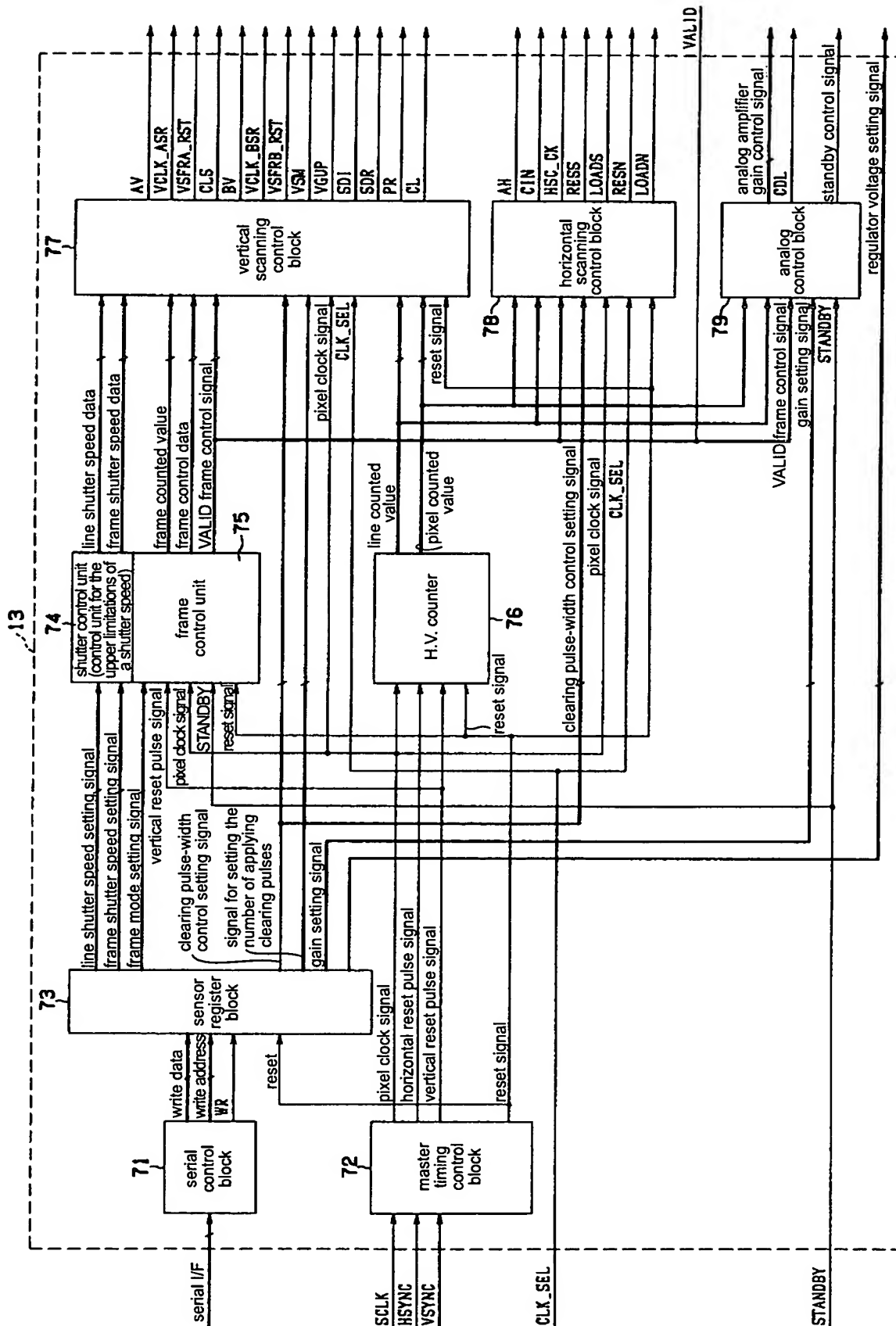


FIG. 7



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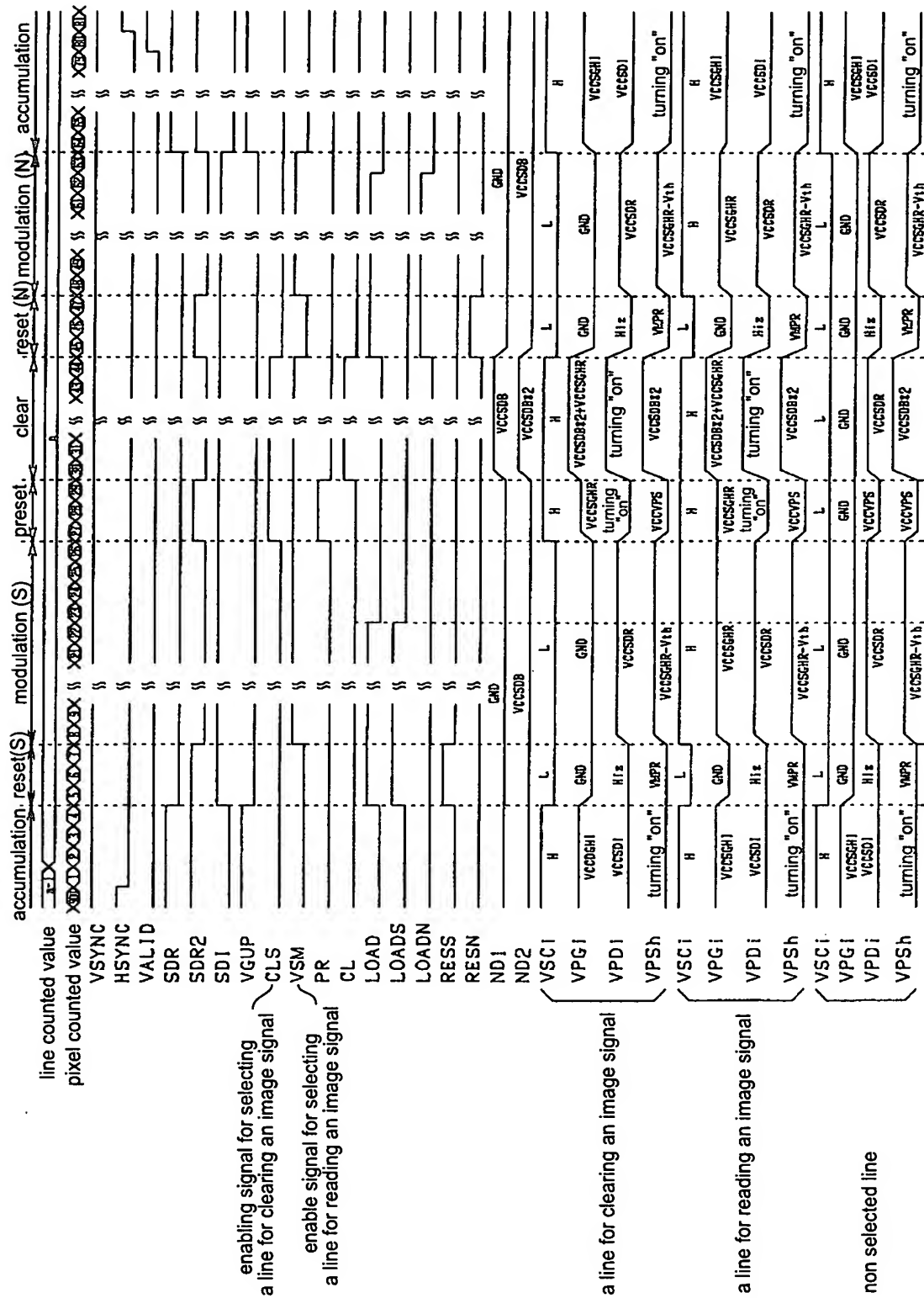


FIG. 9

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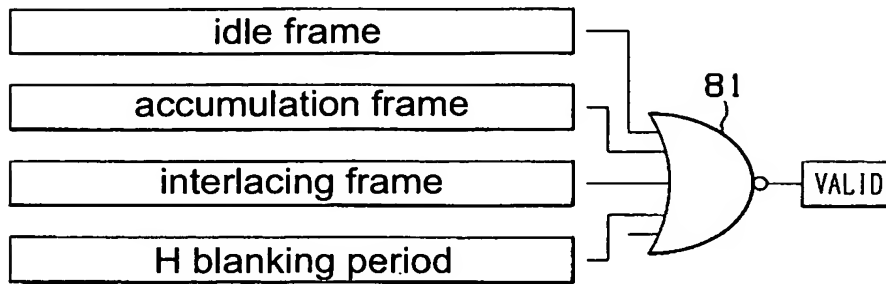


FIG. 10A

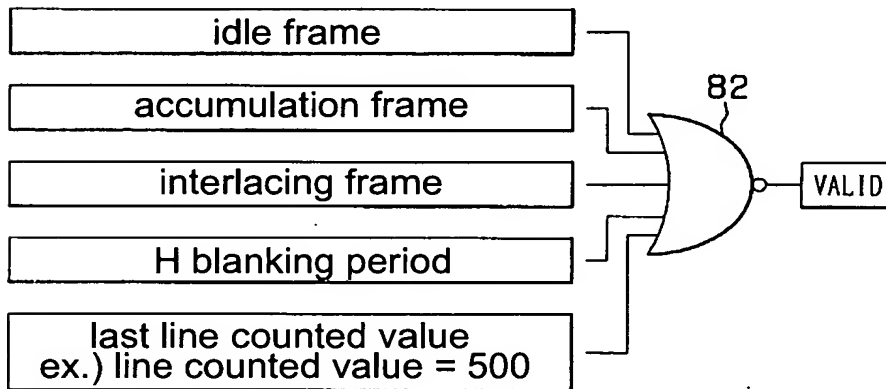


FIG. 10B

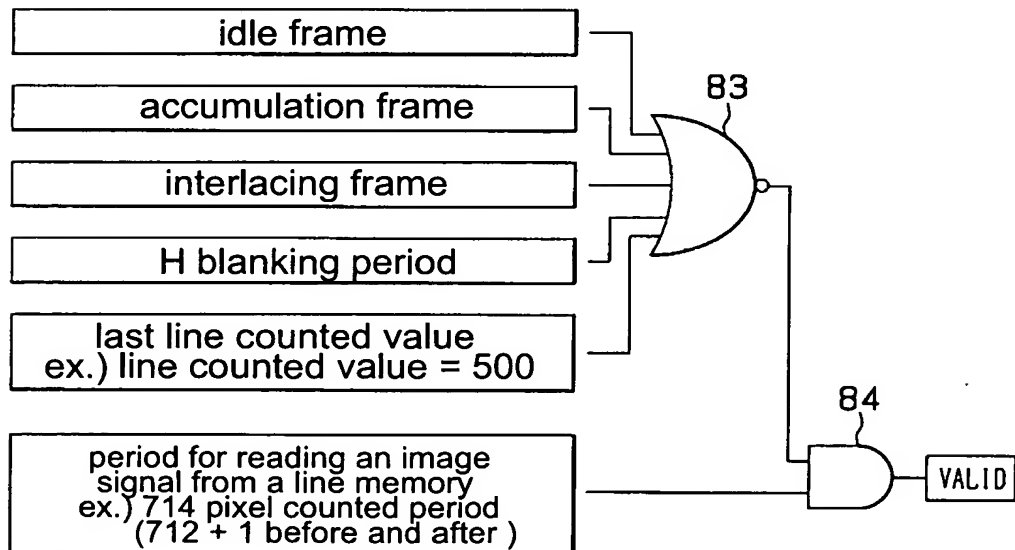


FIG. 10C

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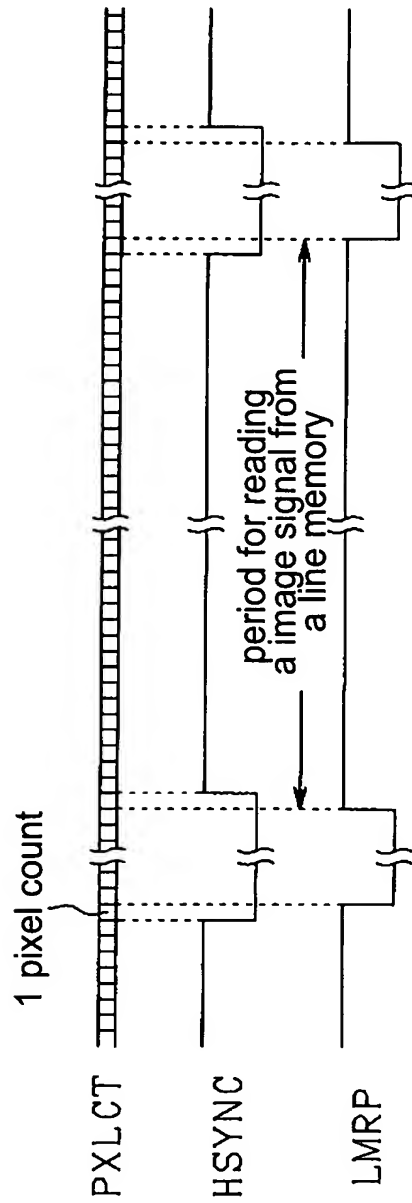


FIG. 11

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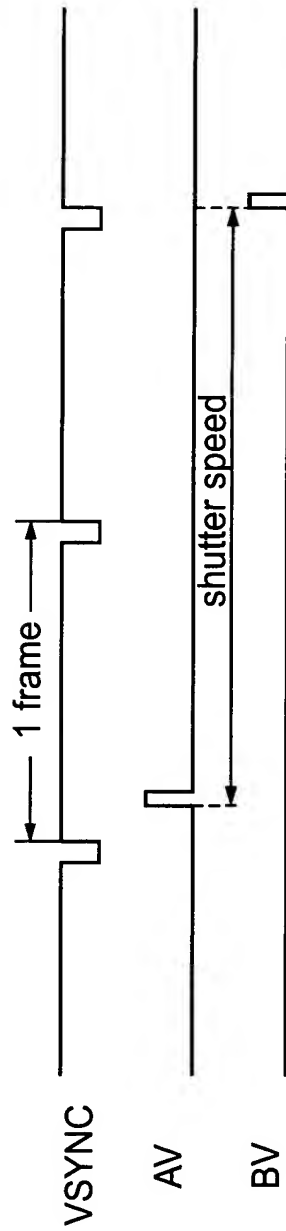


FIG. 12

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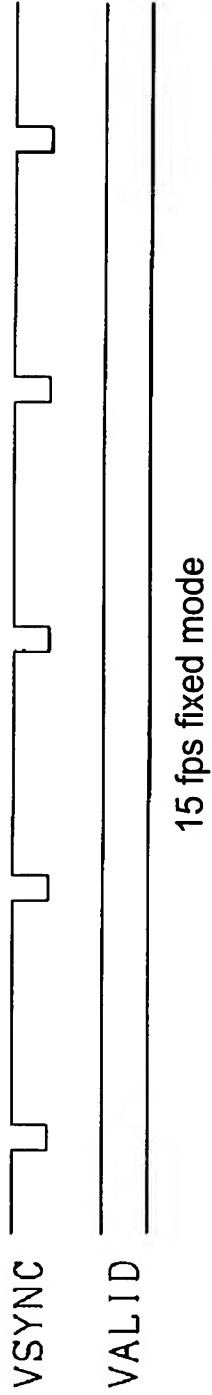


FIG. 13A

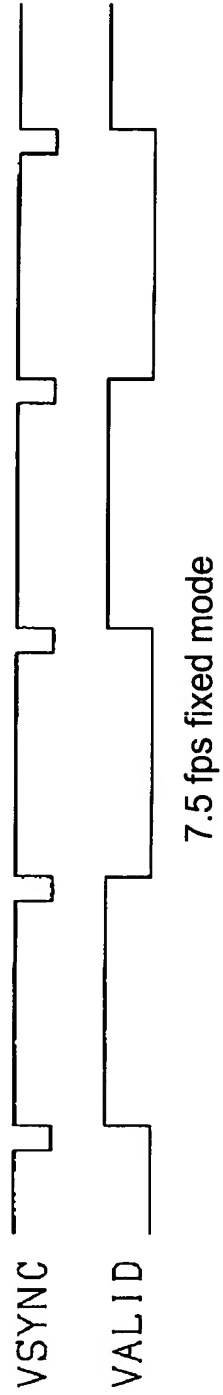


FIG. 13B

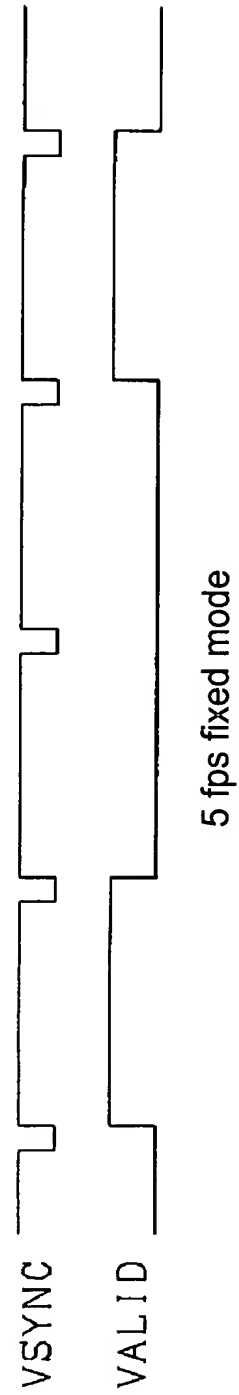


FIG. 13C